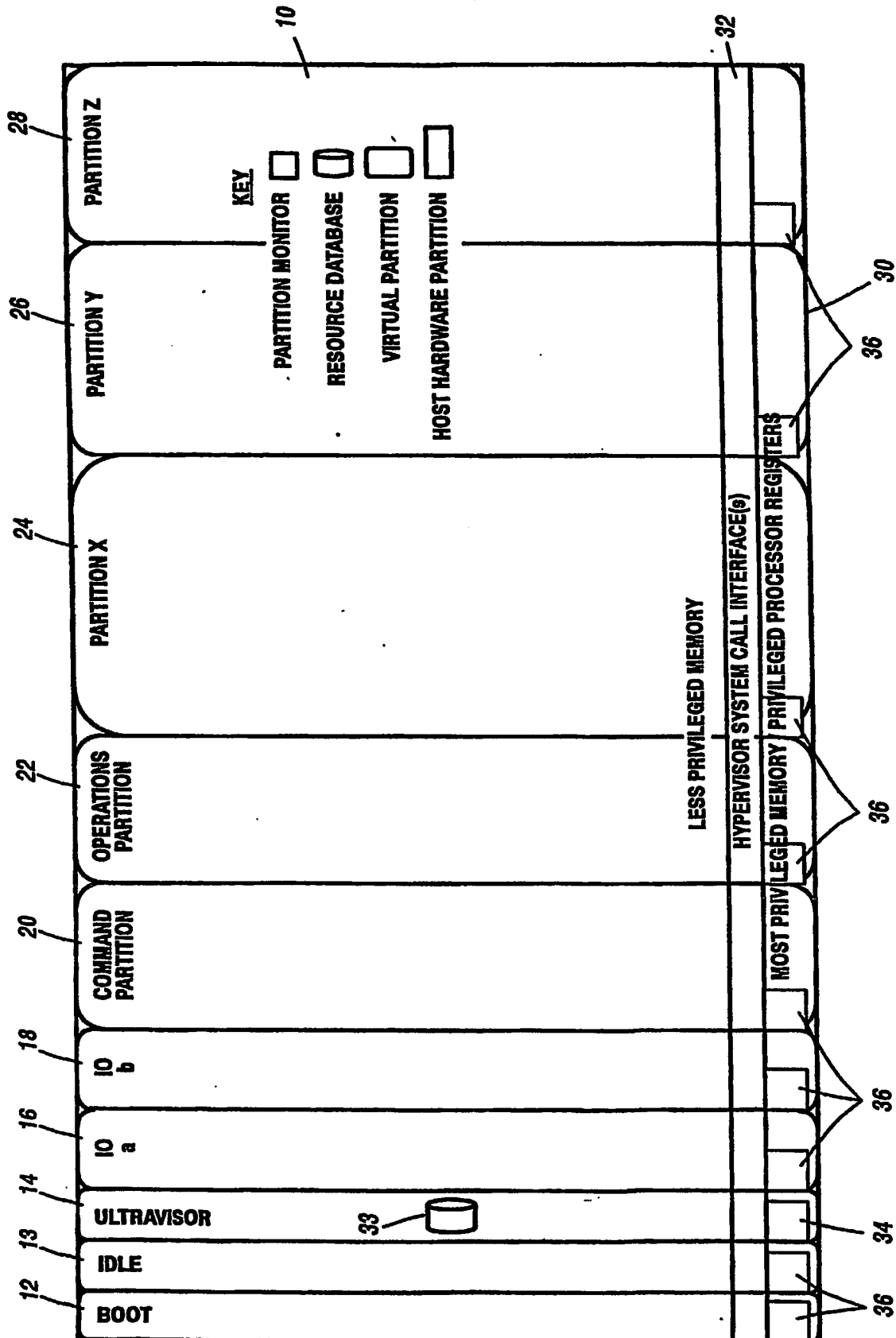


Partitioned Host System

**Figure 1**

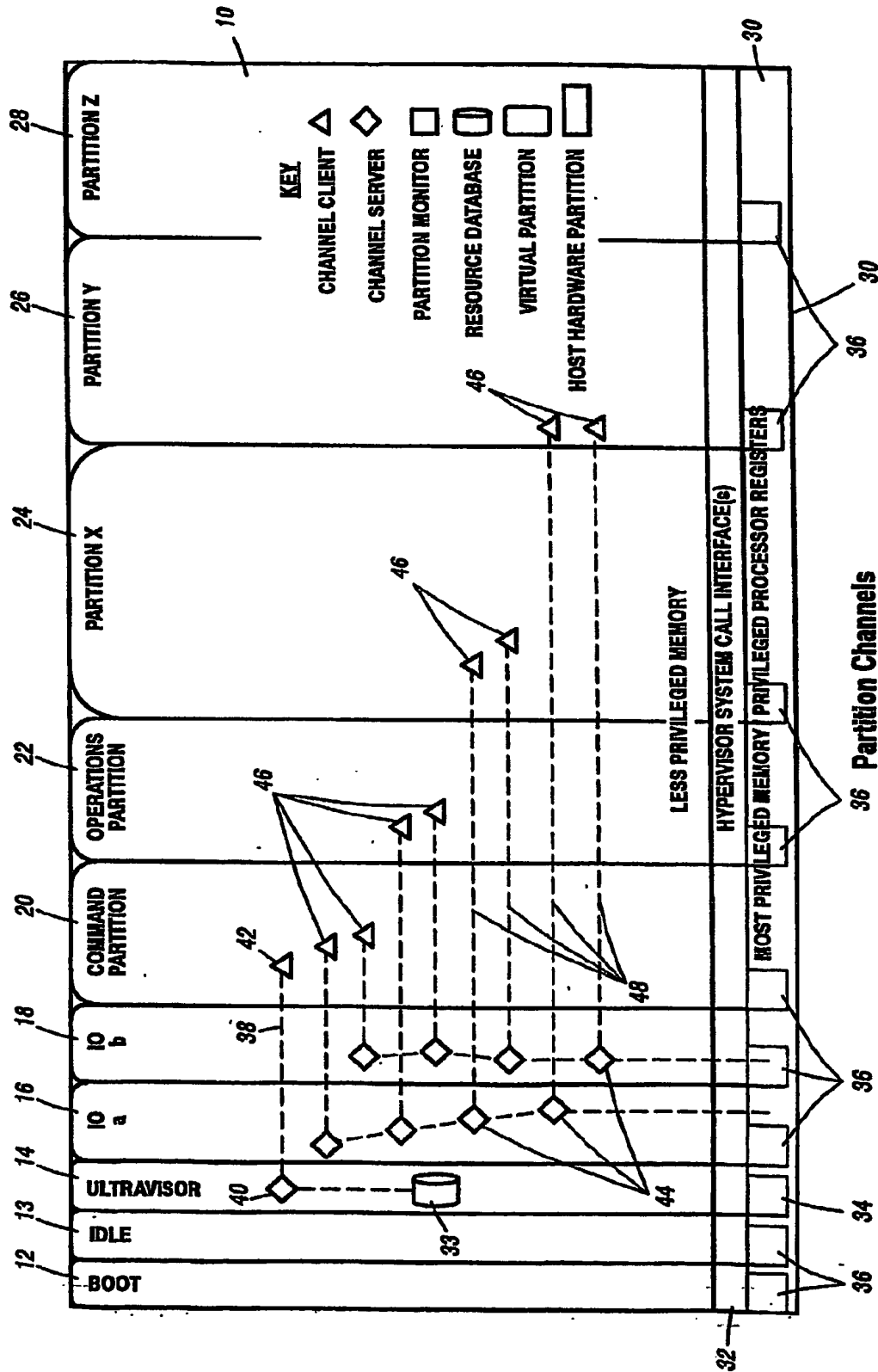
2/15



Partition Monitors

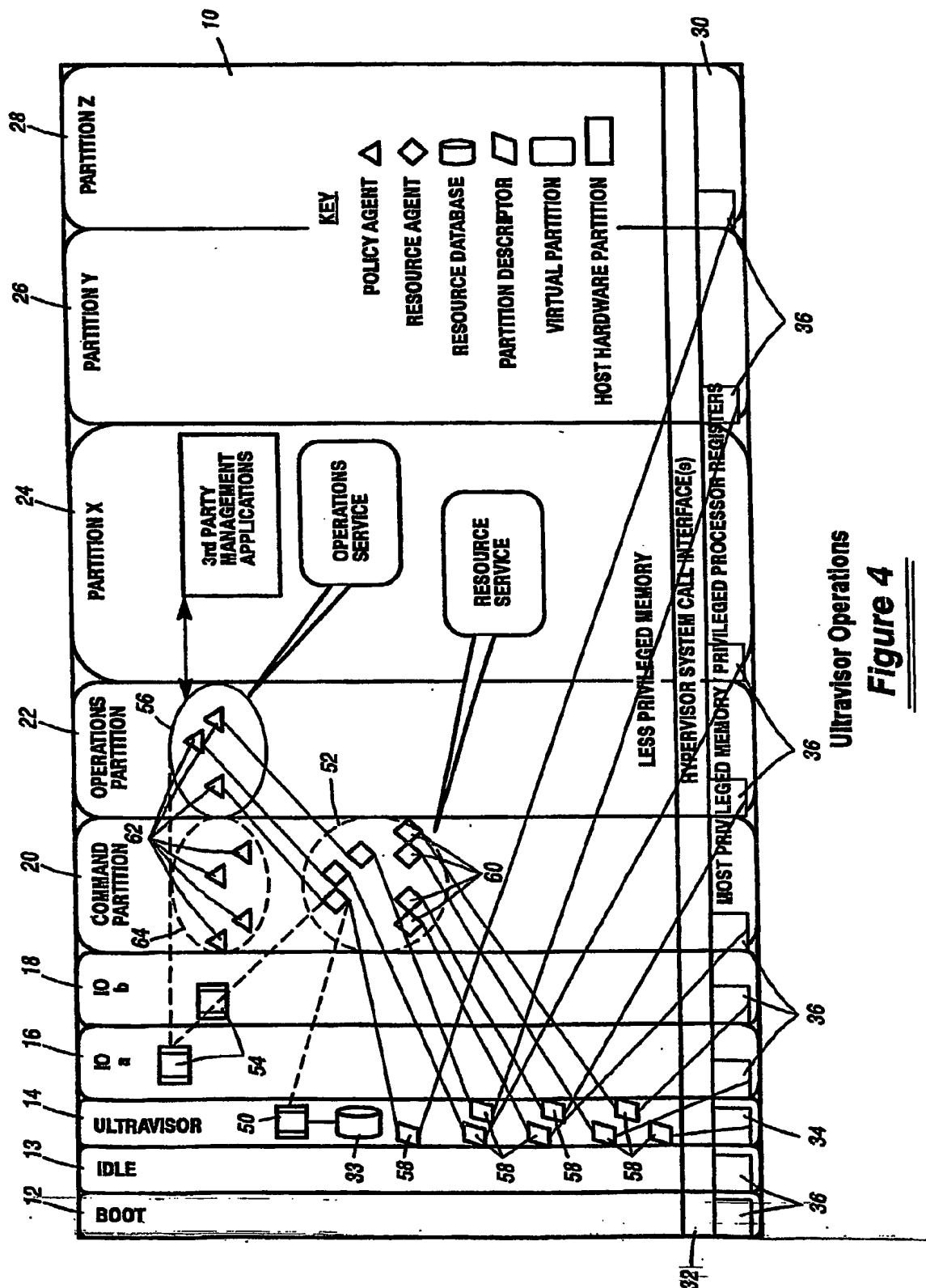
Figure 2

3/15



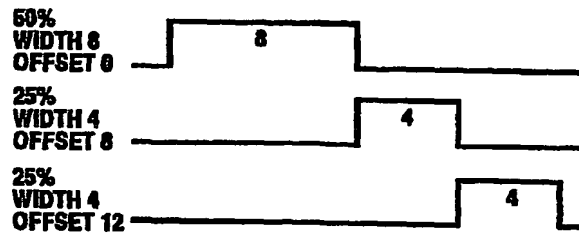
Partition Channels  
Figure 3

4/15



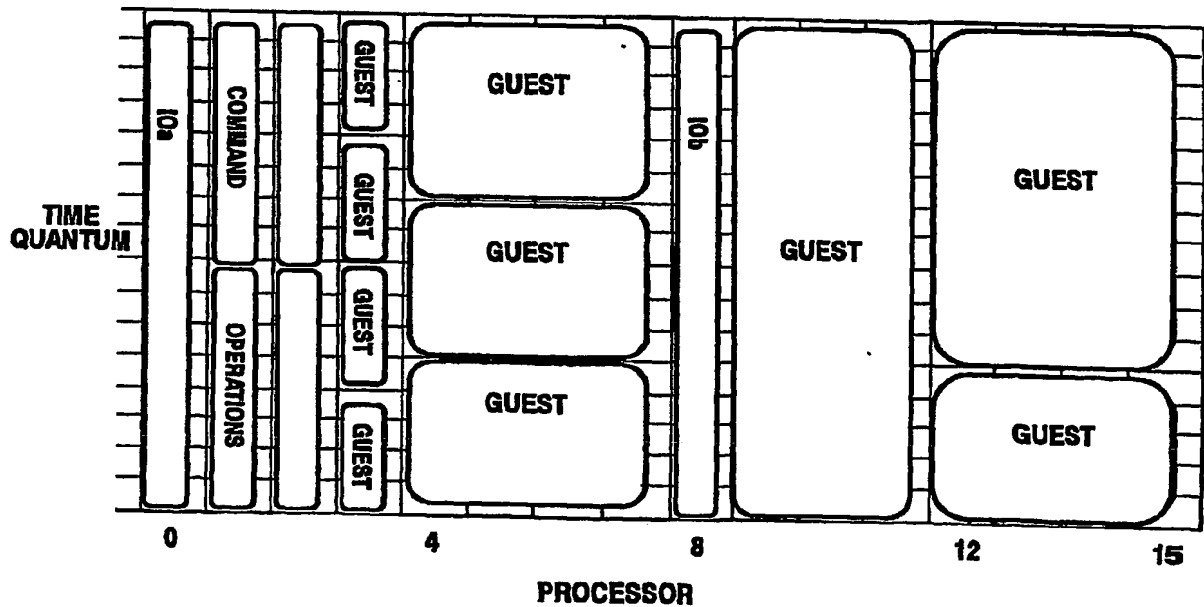
5/15

**OVERLAPPED PROCESSOR THROTTLING - DEFAULT 4 BIT DUTY WIDTH ( $1/16 = 6.25\%$ )**



**Processor Sharing**

**Figure 5**



**Processor Schedule**

**Figure 6**

6/15

$(2^{10})^n$	SI Prefix	SI Name	Page Table	Pageantry	IA32/EM32T
1	K	kilo	PKM	PKE	PT/PTE
2	M	mega	PMM	PME	PD/PDE
3	G	giga	PGM	PGE	PDP
4	T	tera	PTM	PTE	PML4
5	P	peta	PPM	PPE	
6	E	Exa	PEM	PEE	
7	Z	zetta	PZM	PZE	
8	Y	yotta	PYM	PYE	

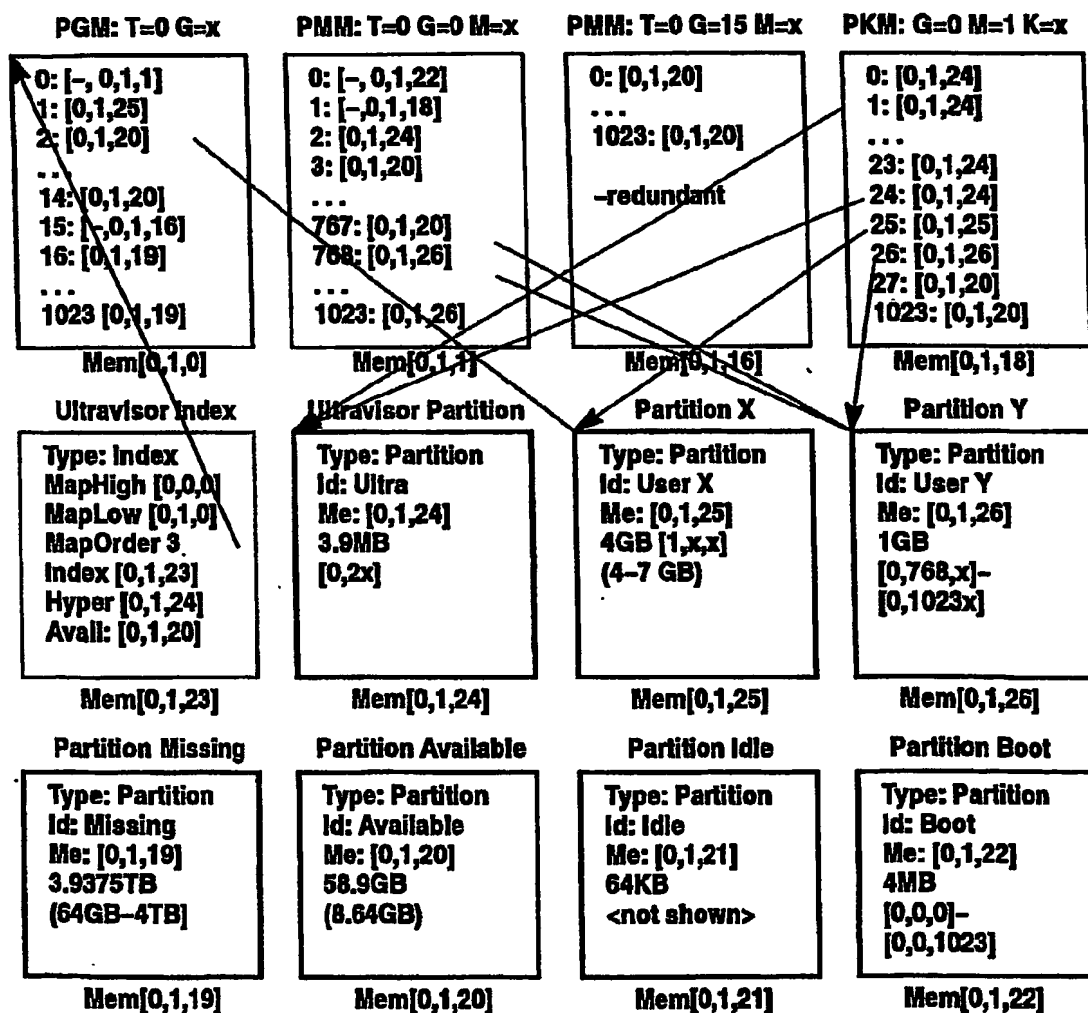
SI Prefixes for Page Table Hierarchy

**Figure 7**

7/15

Function GetMemoryOwner [T : 0..1023, G:0..1023, M:0..1023, K:0..1023] of Int32

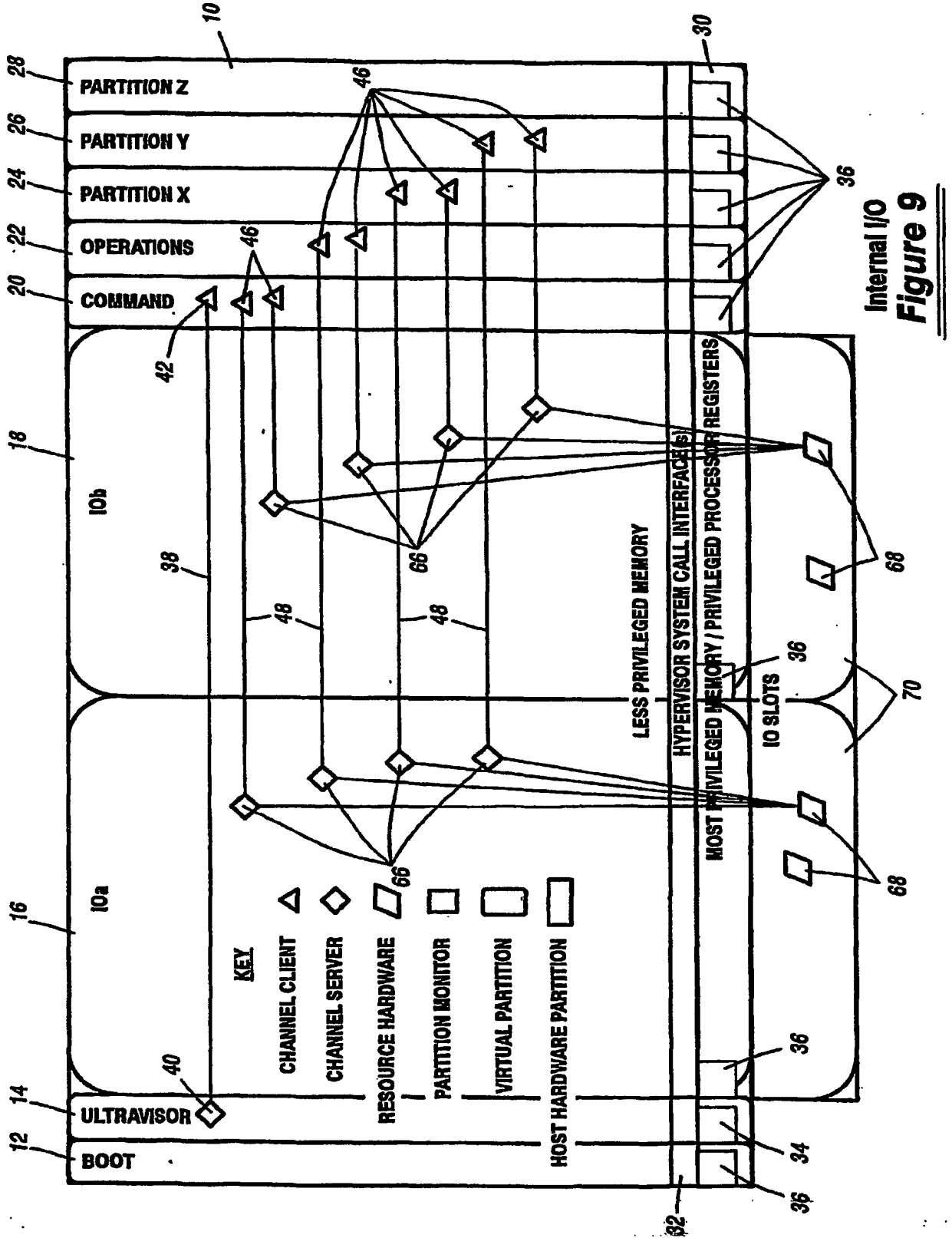
Memory Index Ref	1	GB	MB	KB
Partition number (32 bits)	O S	Node (GB)	MB	KB



Partition Memory Map

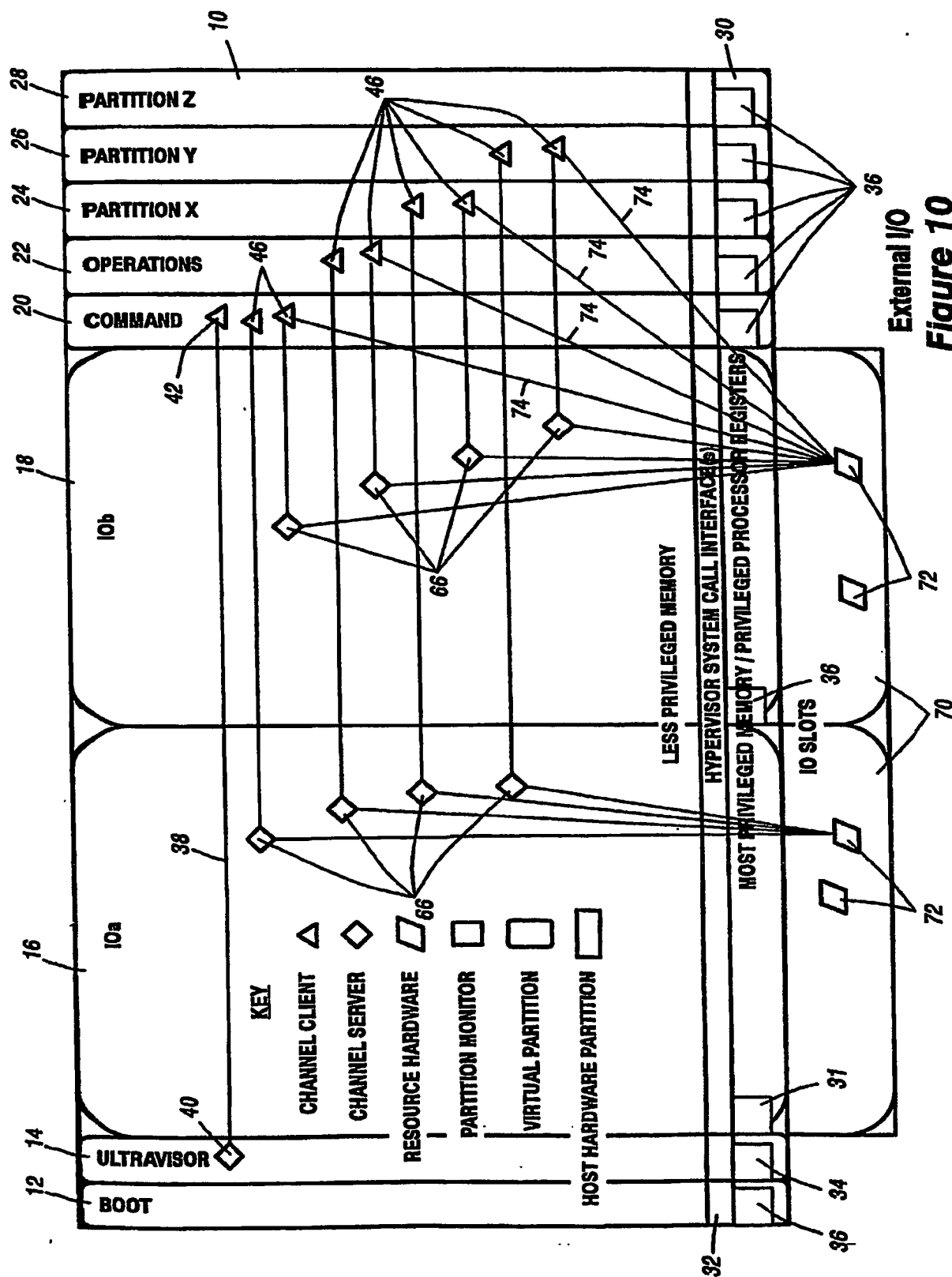
Figure 8

8/15

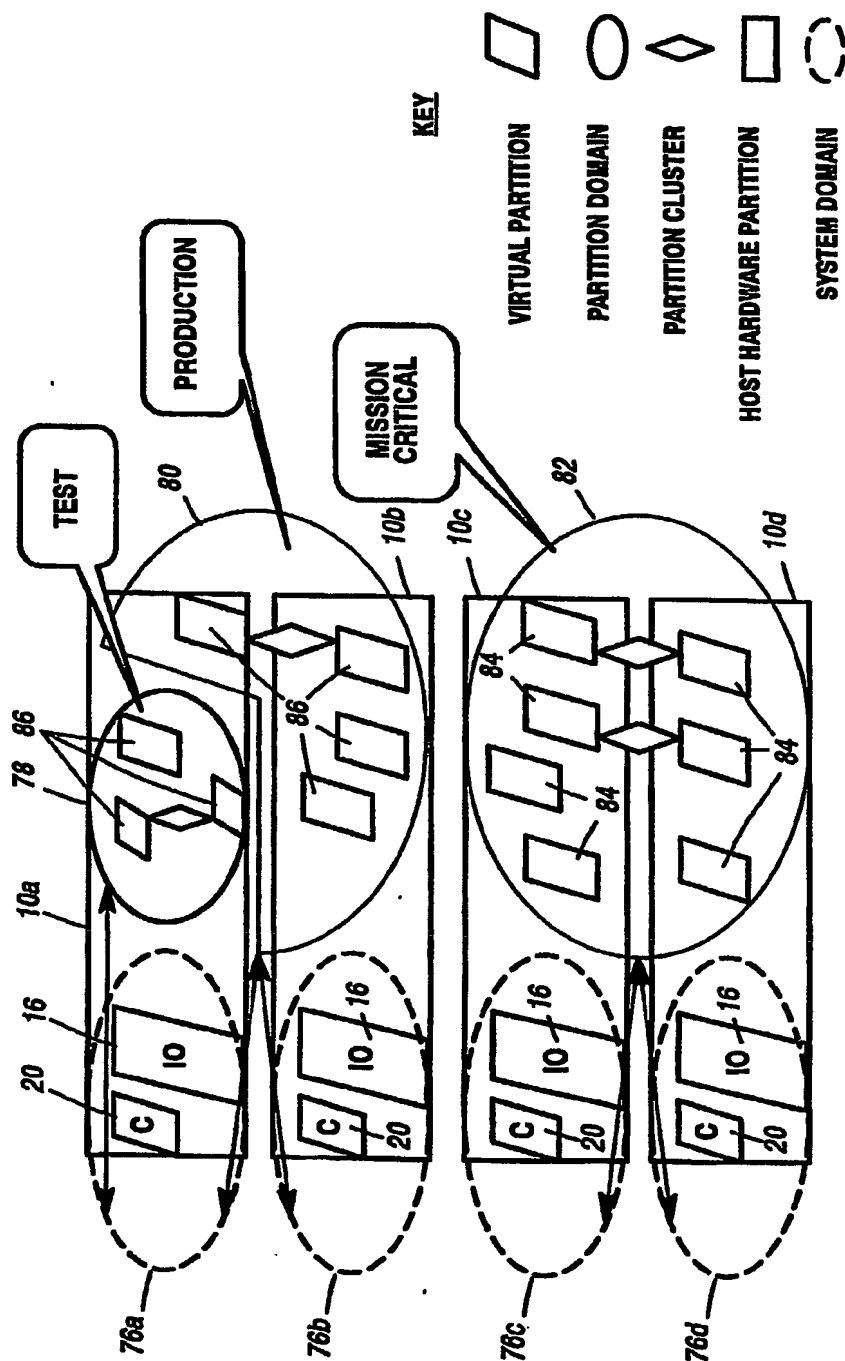




9/15



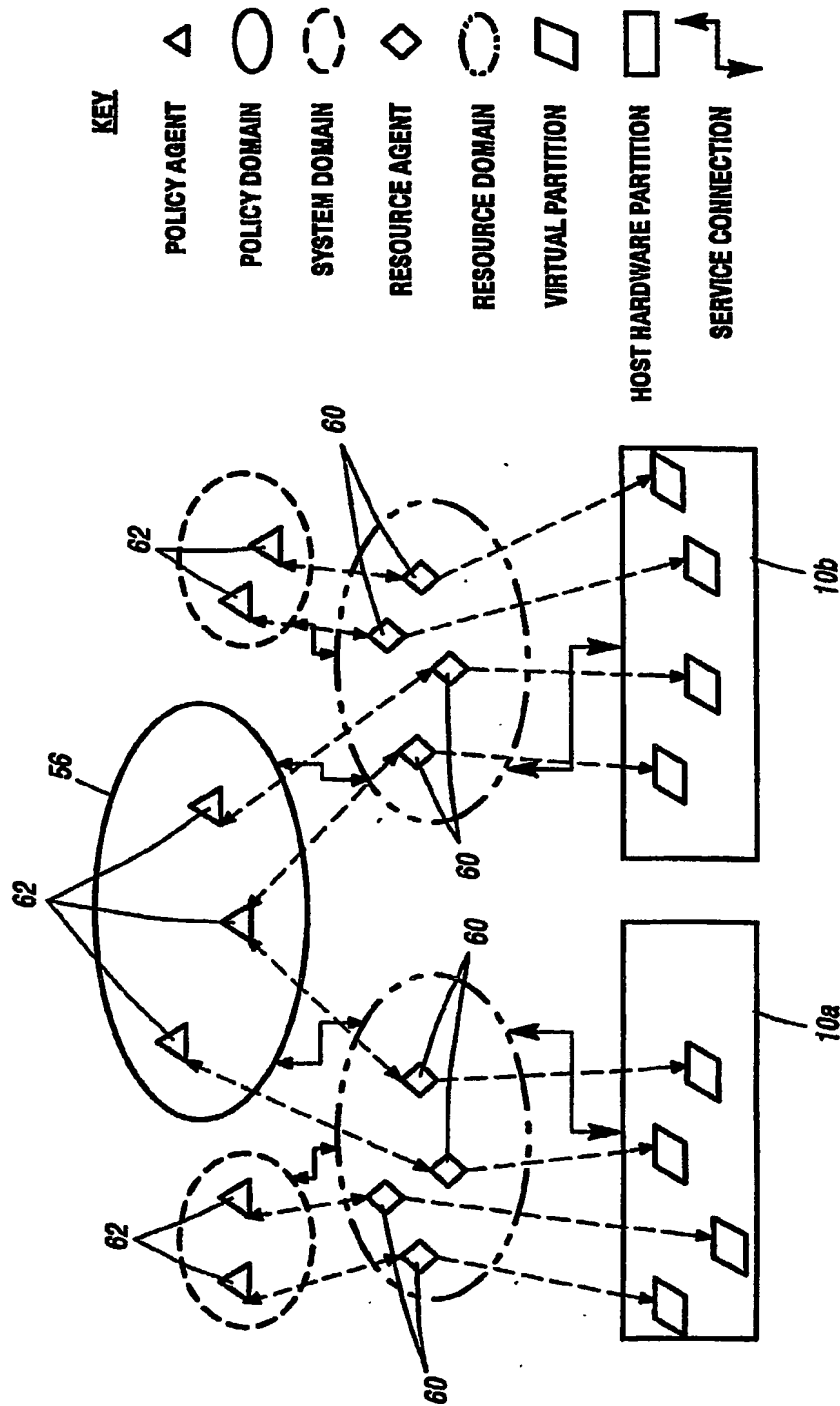
10/15



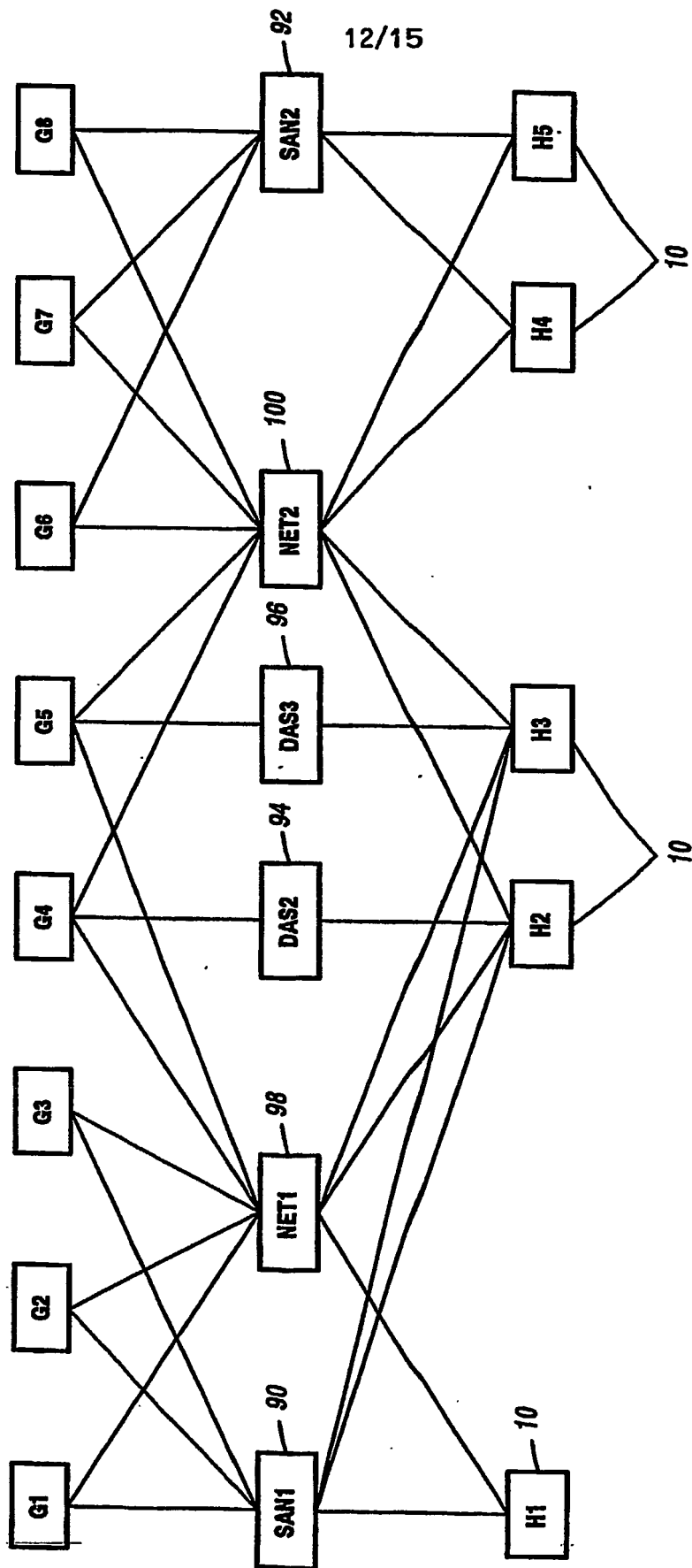
Ultravisor Domains

**Figure 11**

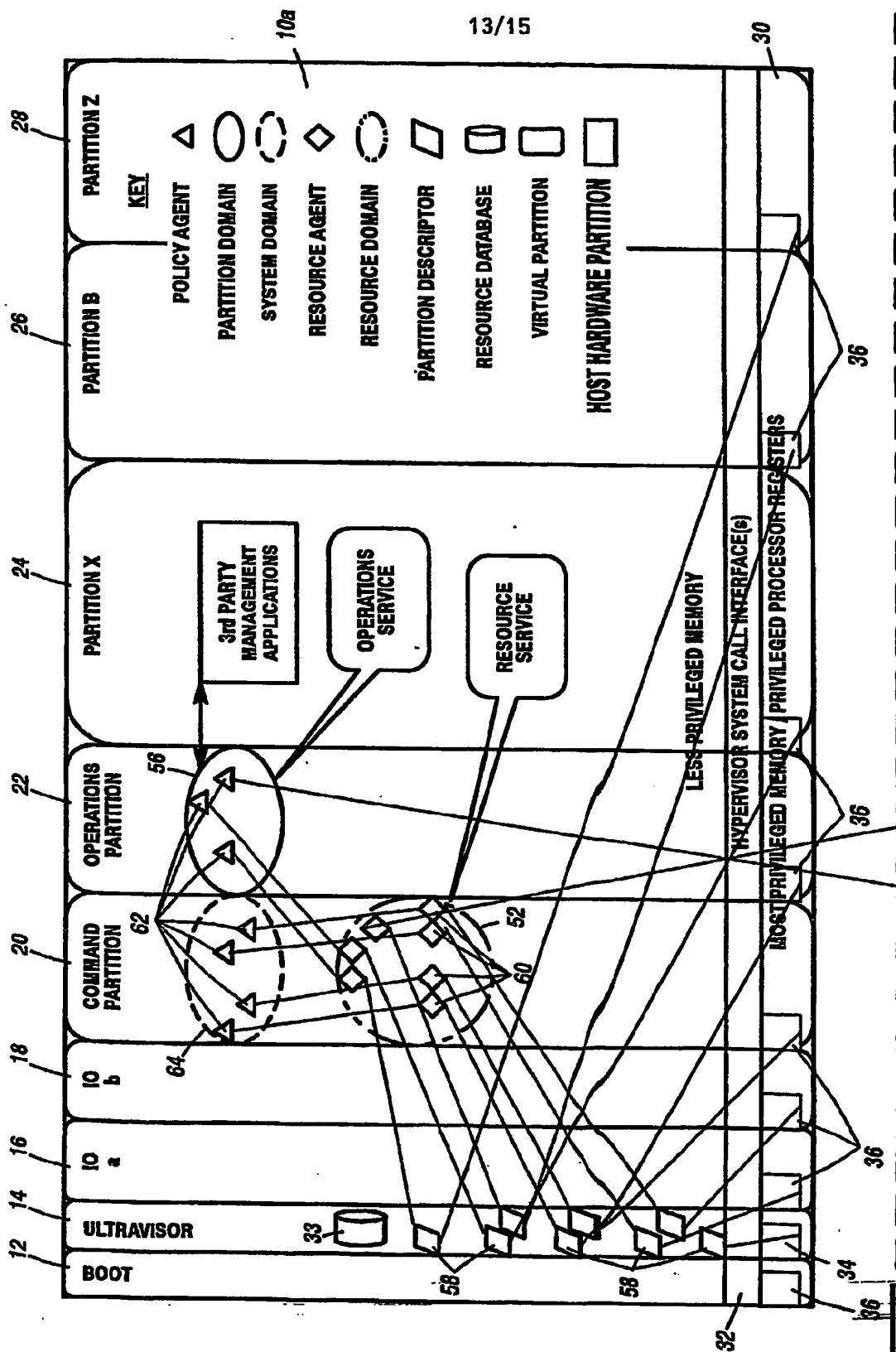
11/15



**Ultravisor Partition Agents**  
**Figure 12**



Virtual Data Center Zones  
**Figure 13**



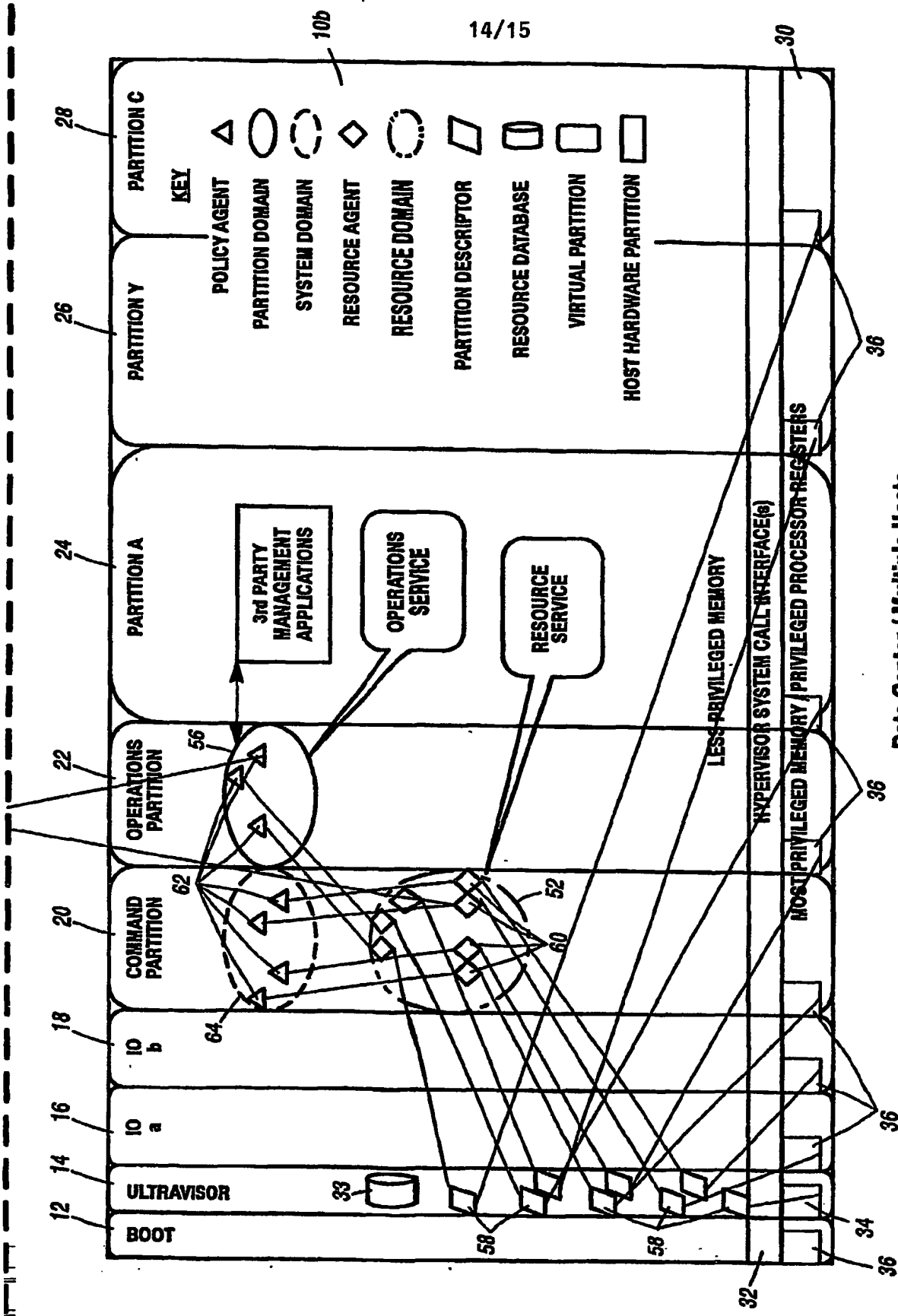
Data Center / Multiple Hosts

Figure 14A

Figure 14

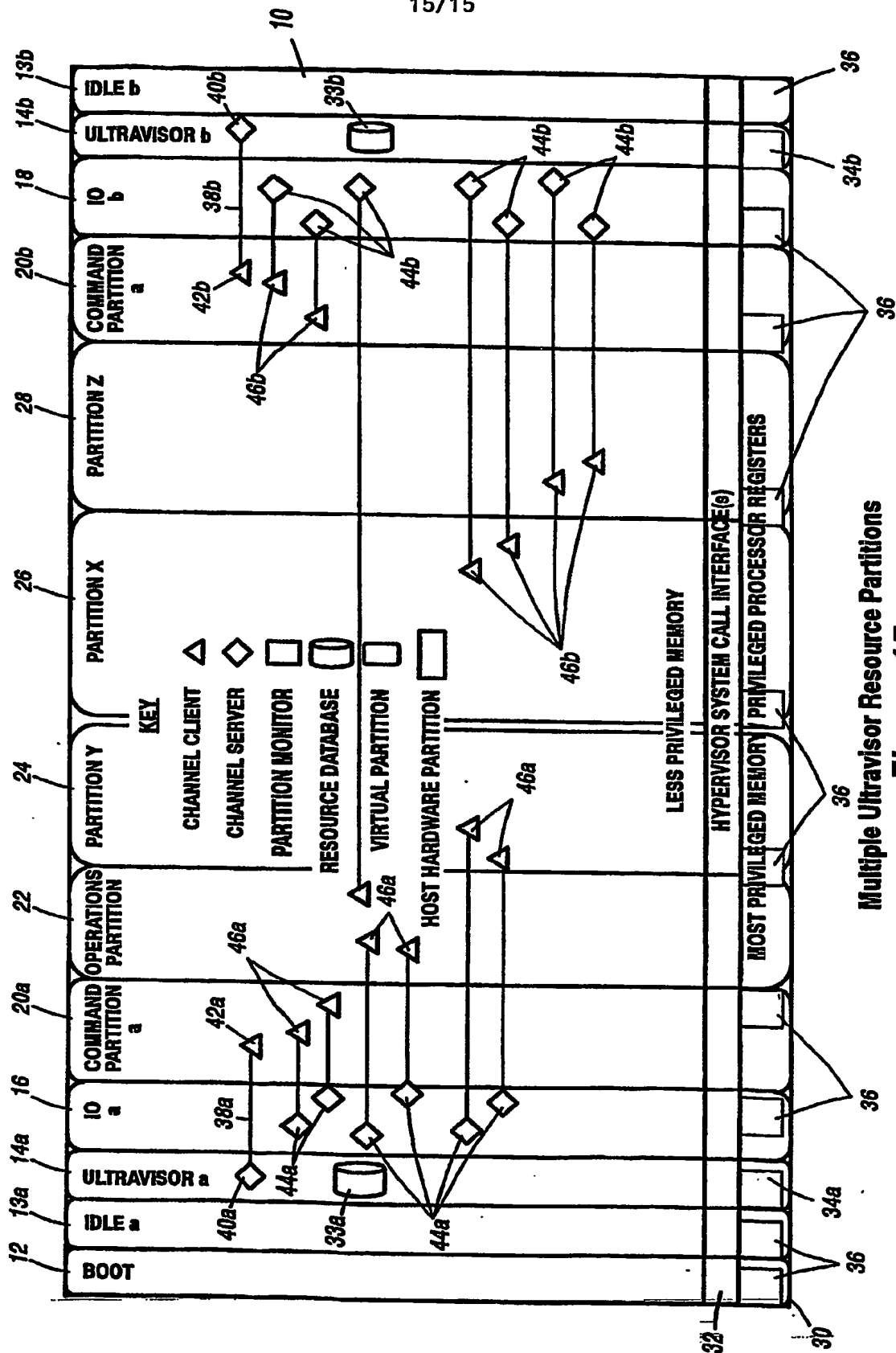
Fig. 14A

Fig. 14B



Data Center / Multiple Hosts  
**Figure 14B**

15/15



Multiple Ultravisor Resource Partitions

Figure 15